

AD8152 Assembly Site Transfer and Data Sheet Revision

Qualification Results Summary of AD8152 BGA_ED Package Extension

QUALIFICATION PLAN / STATUS			
TEST	SPECIFICATION	SAMPLE SIZE	RESULTS
Highly Accelerated Stress Test (HAST)*	JEDEC <i>JESD22-A110</i>	3*32	Pass
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3*32	Pass
High Temperature Storage Life (HTSL)	JEDEC <i>JESD22-A103</i>	1*32	Pass
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3*11	Pass
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JS-002</i>	3/voltage	Pass 750V

*Preconditioned per JEDEC/IPC J-STD-020